

Remarks

Receipt is acknowledged of the Office Action mailed August 9, 2004. Claims 1-11 were pending in the application. Claims 2 and 8 have been amended to be in independent form. Claim 1 has been canceled without prejudice or disclaimer. No new matter has been introduced. Thus claims 2-11 are pending for reconsideration at this time.

Applicants thank the Examiner for acknowledging receipt of the priority documentation in the pending application and for the continuing examination of the pending application.

Allowable Subject Matter

Applicants thank the Examiner for the indication of allowable subject matter in claim 5 and claim 8. Claims 5 and 8 presently stand objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim 5 is dependent upon claim 2, and believed to be allowable for at least the following reasons with respect to claim 2.

Claim 8 has been amended to be in independent form. Thus, claim 8 is now believed to be in condition for allowance. Claims 9-11 are dependent upon claim 8, and are also believed to be in condition for allowance.

Allowance of claims 5 and 8-11 is solicited.

Rejections Under 35 U.S.C. §102(b)

Claims 1-4, 6-7 and 9-11 stand rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 4,902,910 to Hsieh ("Hsieh" hereafter). Claims 9-11 are dependent upon allowable claim 8 as noted above. Thus, claims 9-11 cannot be anticipated by Hsieh for at least the reasons acknowledged by the Office Action, in addition to the further patentable features recited therein. Claim 1 has been canceled without prejudice or disclaimer. Applicants respectfully traverse the rejection of claims 2-4, 6 and 7 for at least the following reasons.

As shown in Fig. 8, Hsieh discloses a power-on reset circuit 100 and a power supply voltage sensing circuit 90. The Office Action asserts that Hsieh discloses "a second noise filter (100 and 202)" as part of the reset circuit 100 and power supply voltage sensing circuit 90. Applicants respectfully disagree. Fig. 8 of Hsieh is reproduced below for the Examiner's convenience.

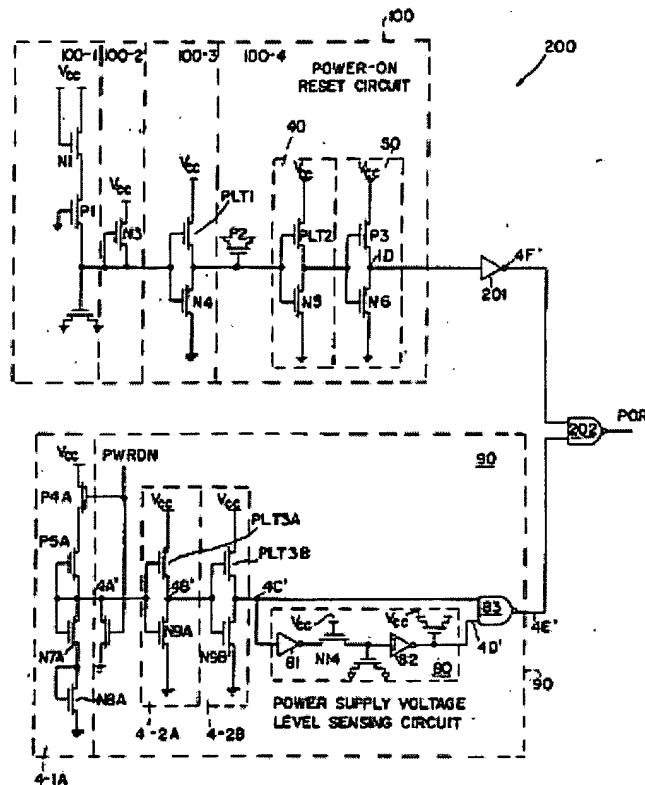


FIG. 8

As shown above, Hsieh discloses a power-on reset circuit 100 (col. 12, lines 6-9) and NAND gate 202 (col. 12, lines 9-13). The power-on reset circuit 100 generates a reset signal just when a power supply voltage is lower than a predetermined voltage. NAND gate 202 NANDs output signals from the power-on reset circuit 100 and a power supply voltage level sensing circuit 90 in order to generate a reset signal when a power supply voltage level suddenly drops lower than a selected value as well as when a power supply voltage is applied to a circuit. Thus, the power-on reset circuit 100 and NAND gate 202 of Hsieh do not operate so as to change the level of a comparison result when an output signal from a first noise filter is maintained at a low level *over a predetermined period* as presently claimed. Hence, Hsieh fails to disclose or suggest the claimed second noise filter and, as such, Hsieh fails to anticipate the presently claimed invention.

Withdrawal of the rejection under 35 U.S.C. §102(b) is solicited.

Conclusion


In view of the above amendments and remarks, Applicants respectfully request that all objections and rejections be withdrawn and that a notice of allowance be forthcoming. The Examiner is invited to contact the undersigned for any reason related to the advancement of this case.

The Commissioner is authorized to credit any over payment or charge any deficient to deposit account number 08-1641.

Respectfully submitted,

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